

Comms

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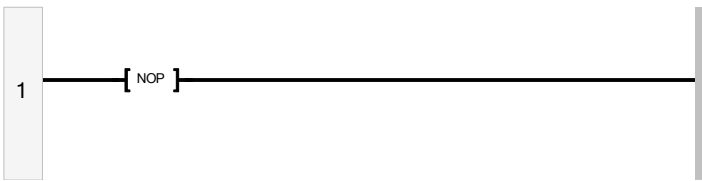
Local Variables

Name	Alias	Data Type	Dimension	Initial Value	Project Value	Comment	Direction	String Size
Int_Array_Write		INT	[0..123]		Var	
Int_Array_Read		INT	[0..123]		Var	
Read_Error_Status		CIPSTATUS			Var	
Read_Error_Msg		STRING					Var	80
Path		STRING		'131.151.52.135,1,0'			Var	80
NewVariable		BOOL					Var	
Msg_Tic		TON			Var	
MSG_ClxRd		MSG_CIPSYMBOLIC			Var	
ClxRd_CtrlCfg		CIPCONTROLCFG			Var	
ClxRd_SymCfg		CIPSYMBOLICCFG			Var	
ClxRd_Status		CIPSTATUS			Var	
ClxRd_TargetCfg		CIPTARGETCFG			Var	
ClxRd_Data		USINT	[0..489]		Var	
ClxRd_Q		BOOL					Var	
COP_DintArray		COP			Var	
COP_Dest		DINT	[0..1]		Var	
Inc_Tmr		TON			Var	
Temp_Dint		DINT					Var	
ClxRdReal_SymCfg		CIPSYMBOLICCFG			Var	
ClxRdReal_Data		USINT	[0..489]		Var	
ClxRdReal_Status		CIPSTATUS			Var	
ClxRdReal_Q		BOOL					Var	
Msg_Enable		BOOL		TRUE			Var	
COP_RealWr		COP			Var	
ClxWrReal_Data		USINT	[0..489]		Var	
ClxWrReal_Status		CIPSTATUS			Var	
ClxWrReal_SymCfg		CIPSYMBOLICCFG			Var	
FT101		REAL					Var	
MSG_ClxWrReal		MSG_CIPSYMBOLIC			Var	

Rung1 Diagram

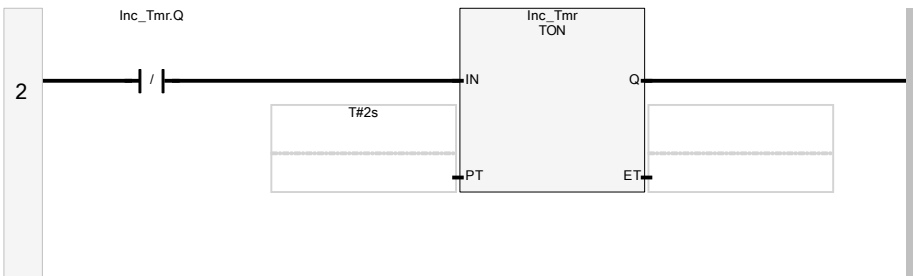
Communication with ControlLogix in chassis, slot 0

IP address is 192.168.1.215



Rung2 Diagram

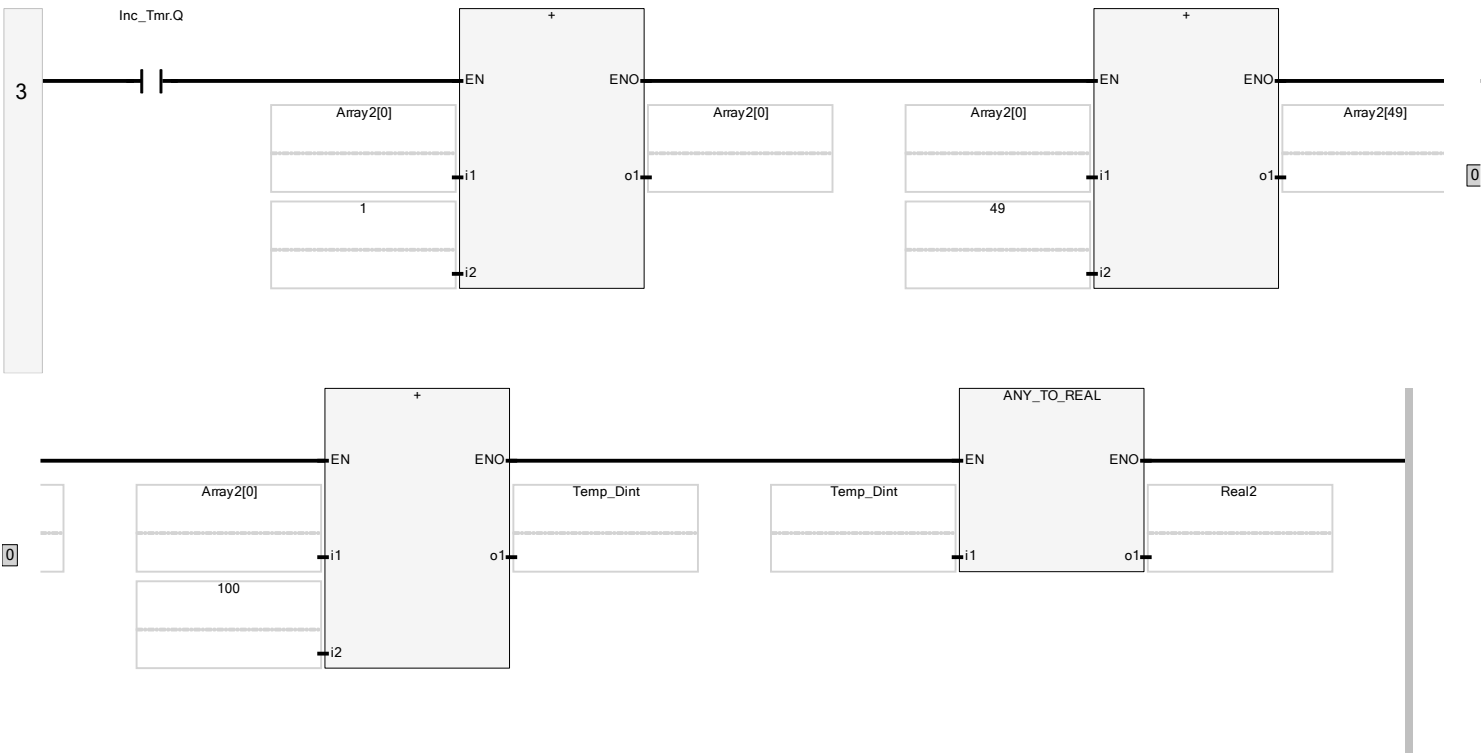
Timer to change data in array and real tag



Rung3 Diagram

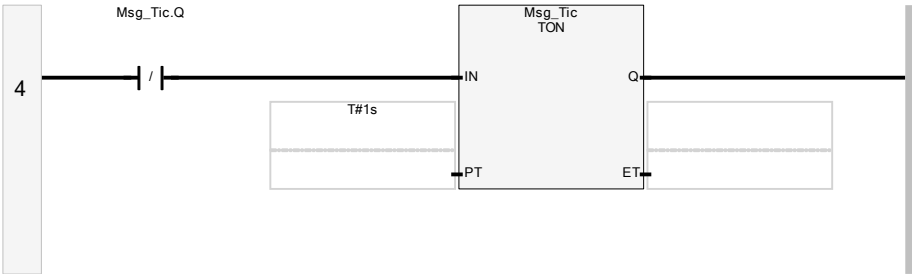
Increment Array2[0], set the last element to Array2[0]+49 and set Real2 to Array2[0]+100.

All of these are read by ControlLogix processor

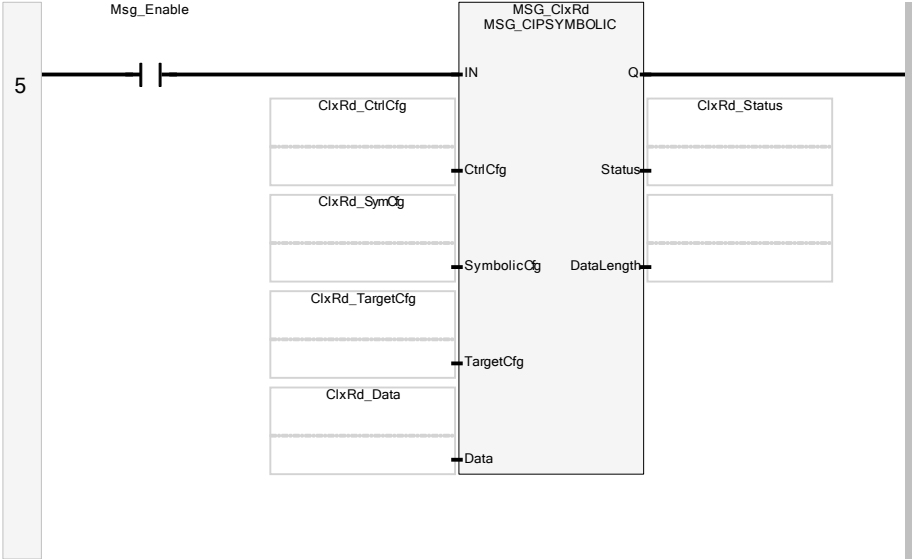


Rung4 Diagram

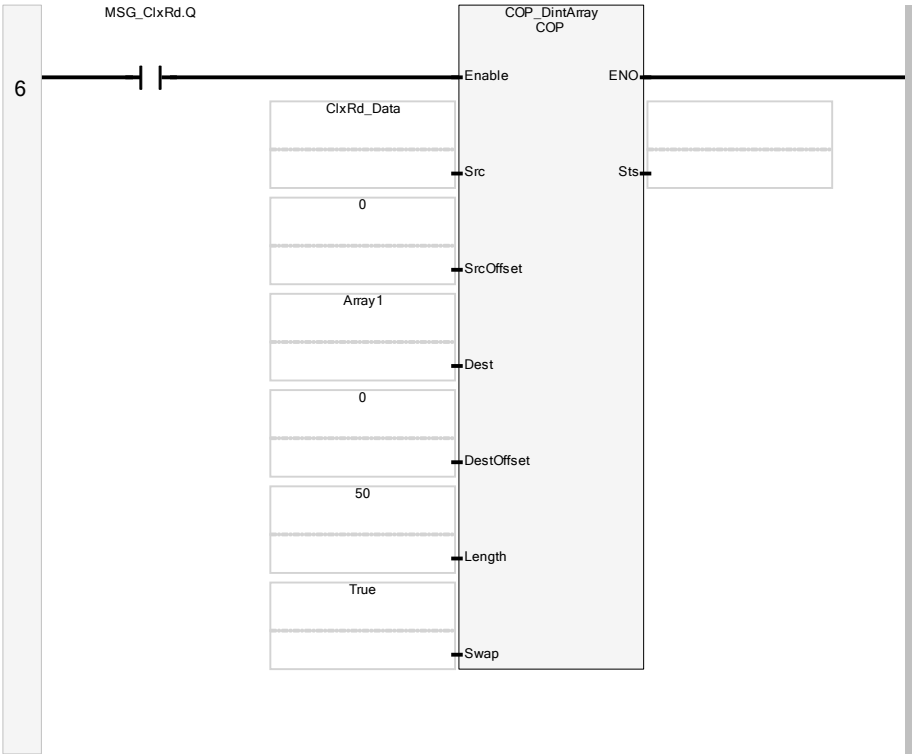
Msg tic for messages



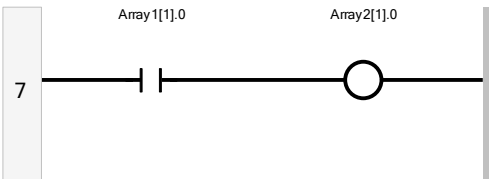
Rung5 Diagram
Read of Array1 (DINT[0..49]) from ControlLogix in chassis, slot 0



Rung6 Diagram
Copy read data bytes into Array1 variable

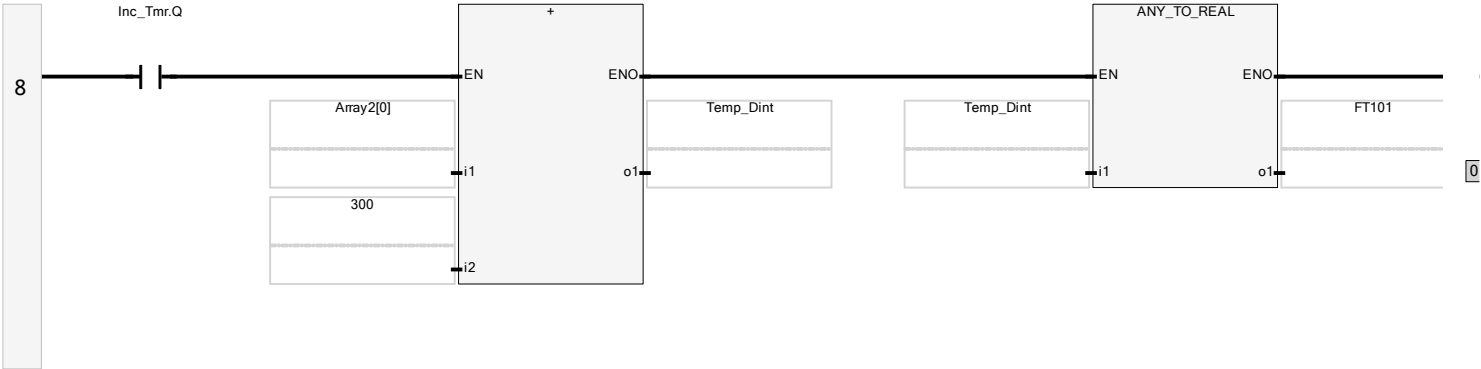


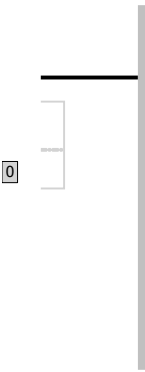
Rung7 Diagram



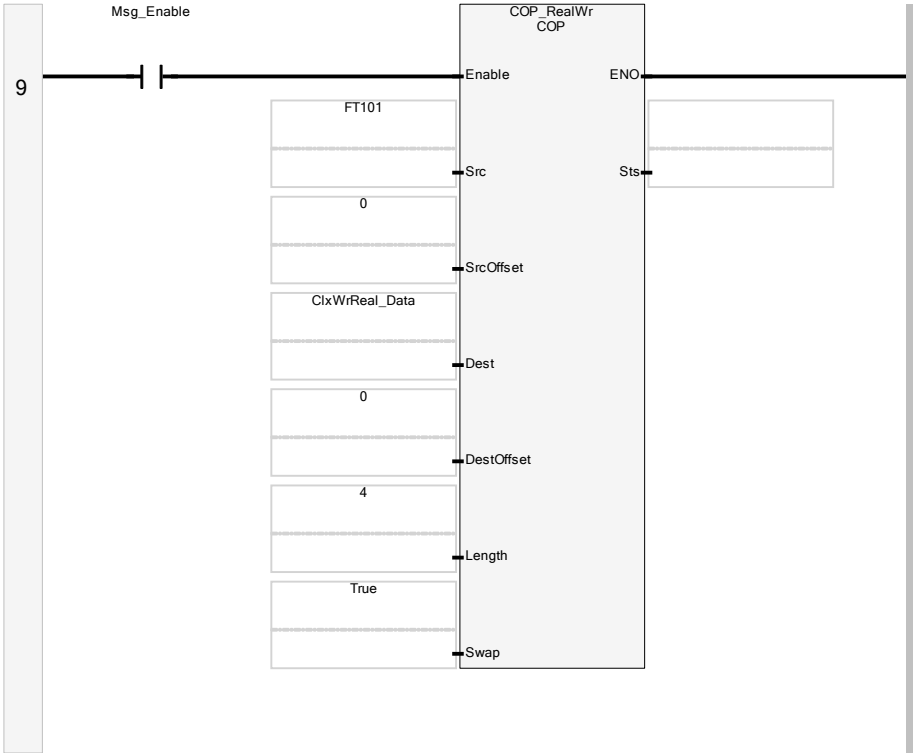
Rung8 Diagram

Make up FT101





Rung9 Diagram
Copy FT101 bytes into data array written to ControlLogix



Rung10 Diagram
Write of FT101 (REAL) to ControlLogix in chassis, slot 0



Rung11 Diagram

