

TECHNICAL DOCUMENTATION

SP6_11

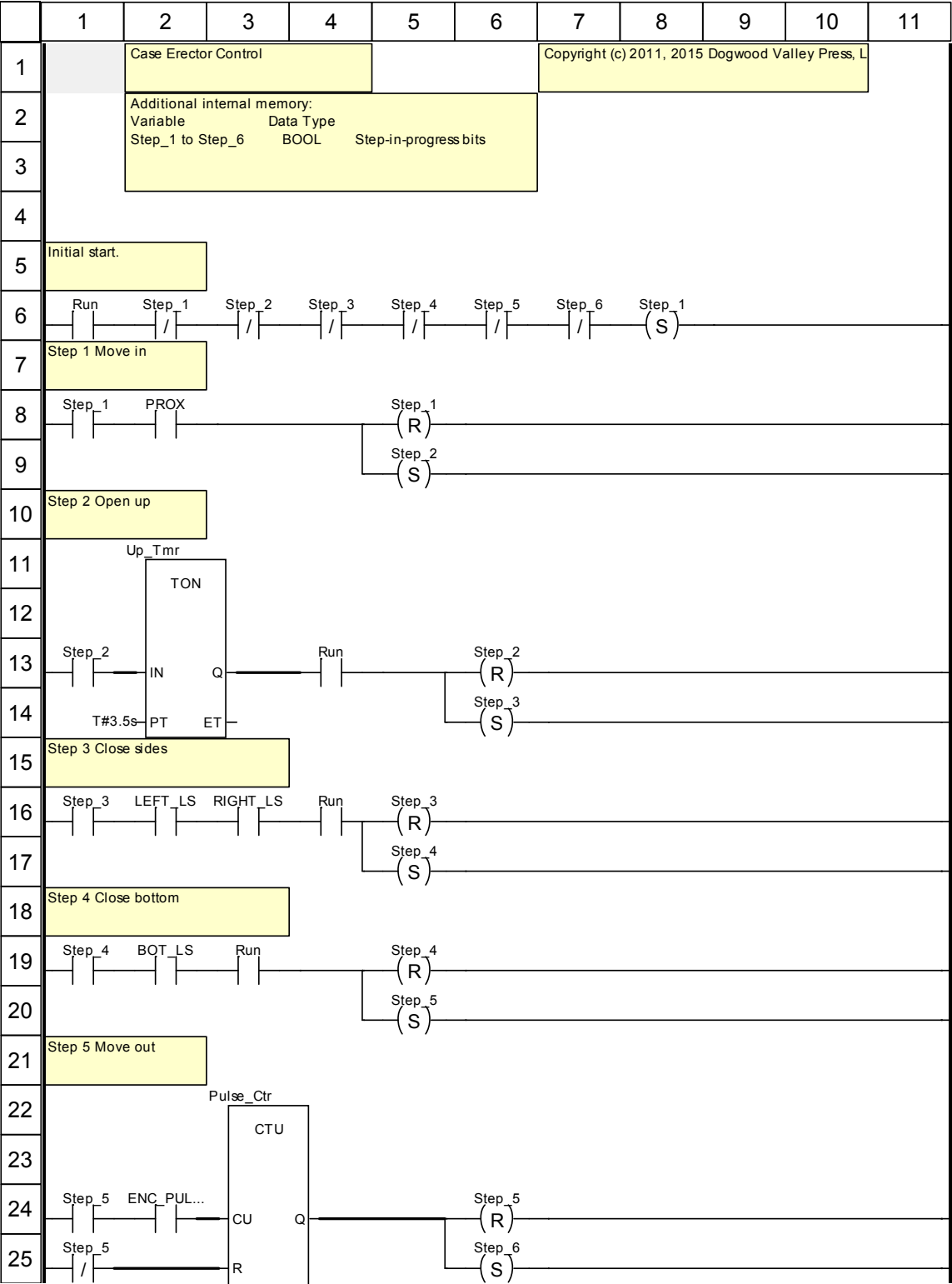
Project	SP6_11
Designer	
Application	sp6_11.stu
Software Version	Unity Pro L V10.0
Creation Date	4/12/2011 8:52:23 PM
Last Modification Date	12/22/2015 4:40:15 PM
Target PLC	BMX P34 1000 02.00CPU 340-10 Modbus

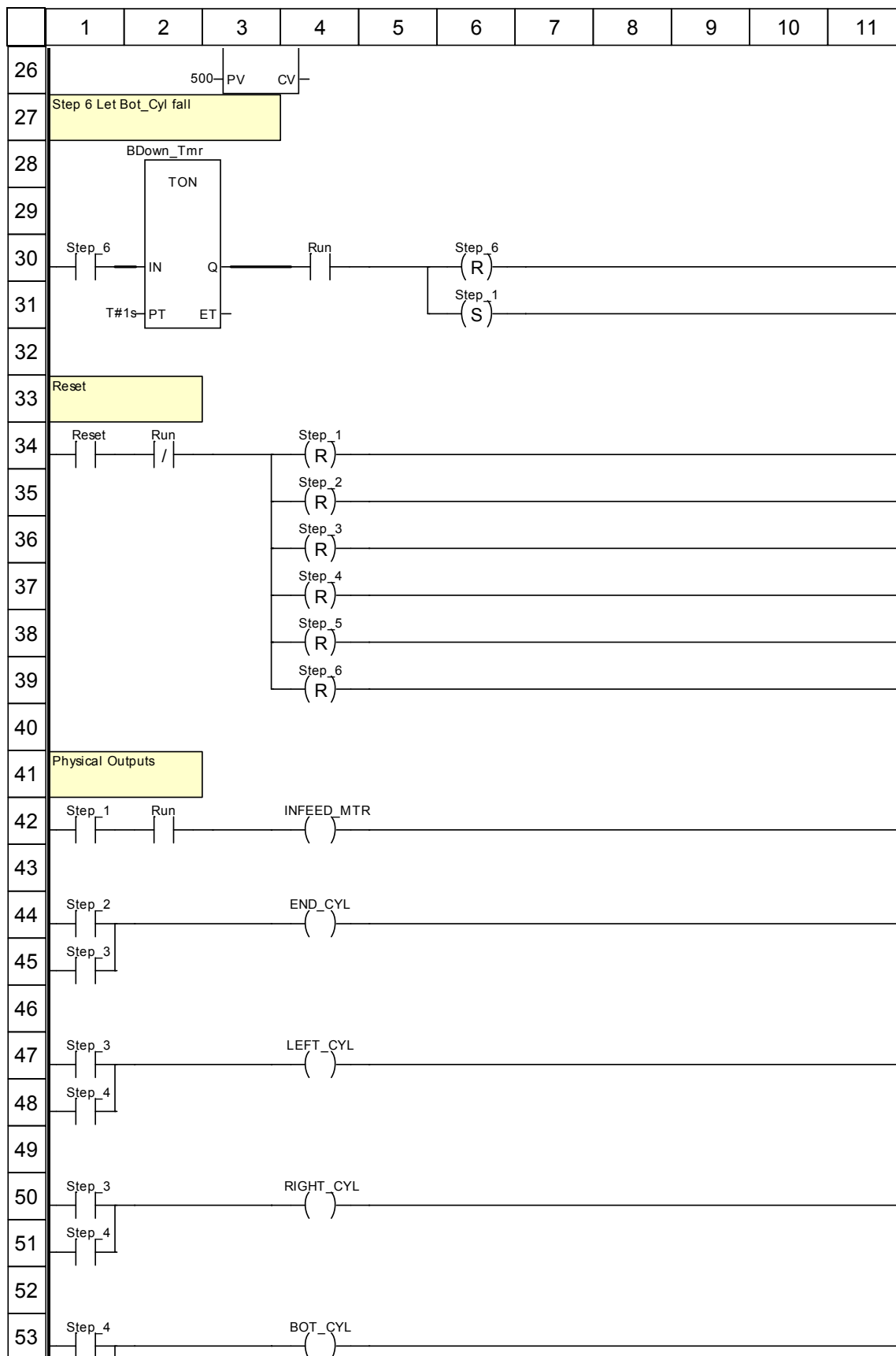
MAST

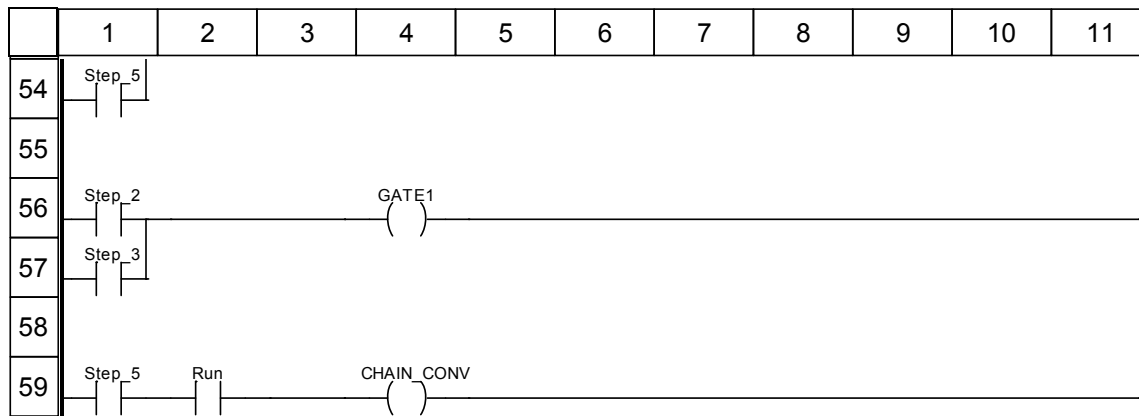
Specific properties

Configuration	Cyclic
Task period configuration	0
Watchdog time configuration	250

main : [MAST]







Truncated labels:

Label	Position(s)
ENC_PULSE	(2, 24)

Cross References

Application:

Addresses

Object	Referred into	Location	Usage
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Variables or FB instances

Object	Referred into	Location	Usage
BDown_Tmr	main : [MAST]	(l 28, c: 2)	FC
		(l 28, c: 2)	R
		(l 28, c: 2)	R
BOT_CYL	main : [MAST]	(l 53, c: 4)	W
BOT_LS	main : [MAST]	(l 19, c: 2)	R
CHAIN_CONV	main : [MAST]	(l 59, c: 4)	W
ENC_PULSE	main : [MAST]	(l 24, c: 2)	R
END_CYL	main : [MAST]	(l 44, c: 4)	W
GATE1	main : [MAST]	(l 56, c: 4)	W
INFEED_MTR	main : [MAST]	(l 42, c: 4)	W
LEFT_CYL	main : [MAST]	(l 47, c: 4)	W
LEFT_LS	main : [MAST]	(l 16, c: 2)	R
PROX	main : [MAST]	(l 8, c: 2)	R
Pulse_Ctr	main : [MAST]	(l 22, c: 3)	FC
		(l 22, c: 3)	R
		(l 22, c: 3)	R
		(l 22, c: 3)	R
RIGHT_CYL	main : [MAST]	(l 50, c: 4)	W
RIGHT_LS	main : [MAST]	(l 16, c: 3)	R
Reset	main : [MAST]	(l 34, c: 1)	R
Run	main : [MAST]	(l 6, c: 1)	R
		(l 13, c: 4)	R
		(l 16, c: 4)	R
		(l 19, c: 3)	R
		(l 30, c: 4)	R
		(l 34, c: 2)	R
		(l 42, c: 2)	R
		(l 59, c: 2)	R
Step_1	main : [MAST]	(l 6, c: 2)	R
		(l 6, c: 8)	W
		(l 8, c: 1)	R
		(l 8, c: 5)	W
		(l 31, c: 6)	W
		(l 34, c: 4)	W
		(l 42, c: 1)	R
Step_2	main : [MAST]	(l 6, c: 3)	R
		(l 9, c: 5)	W
		(l 13, c: 1)	R
		(l 13, c: 6)	W
		(l 35, c: 4)	W
		(l 44, c: 1)	R
		(l 56, c: 1)	R
Step_3	main : [MAST]	(l 6, c: 4)	R
		(l 14, c: 6)	W
		(l 16, c: 1)	R

Cross References

Object	Referred into	Location	Usage
		(l 16, c: 5)	W
		(l 36, c: 4)	W
		(l 45, c: 1)	R
		(l 47, c: 1)	R
		(l 50, c: 1)	R
		(l 57, c: 1)	R
Step_4	main : [MAST]	(l 6, c: 5)	R
		(l 17, c: 5)	W
		(l 19, c: 1)	R
		(l 19, c: 5)	W
		(l 37, c: 4)	W
		(l 48, c: 1)	R
		(l 51, c: 1)	R
		(l 53, c: 1)	R
Step_5	main : [MAST]	(l 6, c: 6)	R
		(l 20, c: 5)	W
		(l 24, c: 1)	R
		(l 24, c: 6)	W
		(l 25, c: 1)	R
		(l 38, c: 4)	W
		(l 54, c: 1)	R
		(l 59, c: 1)	R
Step_6	main : [MAST]	(l 6, c: 7)	R
		(l 25, c: 6)	W
		(l 30, c: 1)	R
		(l 30, c: 6)	W
		(l 39, c: 4)	W
Up_Tmr	main : [MAST]	(l 11, c: 2)	FC
		(l 11, c: 2)	R
		(l 11, c: 2)	R