







 **Controller Example_12_2** **Controller Fault Handler** **Power-Up Handler****Tasks** **MainTask** **MainProgram** **MainRoutine** **Mark_Def_Parts** **Unscheduled****Motion Groups** **Ungrouped Axes****Add-On Instructions****Data Types** **User-Defined** **Strings** **Add-On-Defined** **Module-Defined** **AB:1756_DI:C:0** **AB:1756_DI:I:0** **AB:1756_DO:C:0** **AB:1756_DO:I:0** **AB:1756_DO:O:0** **AB:1756_IF8_Integer:C:0** **AB:1756_IF8_Integer:I:0****Trends****I/O Configuration** **1756 Backplane, 1756-A10** **[0] 1756-L71 Example_12_2** **[1] 1756-IB32/A din** **[2] 1756-OB16I dout**

Copyright (c) 2023 Dogwood Valley Press, LLC		
0	JSR	
	Routine Name	Mark_Def_Parts
(End)		

```
1 (* Example 12.2 Marking Defective Parts *)
2
3 (* Copyright (c) 2023 Dogwood Valley Press, LLC *)
4
5 IF NOT St_Delay.TimerEnable THEN
6   IF ( (PDEFECT AND NOT PROX5) AND NOT Pls_Tmr.DN ) THEN
7     St_Delay.TimerEnable := 1;
8     END_IF;
9 ELSE
10  IF Pls_Tmr.DN THEN St_Delay.TimerEnable := 0;
11 END_IF;
12 END_IF;
13 St_Delay.PRE := 5500;
14 TONR(St_Delay);
15 SPRAY := St_Delay.DN;
16 Pls_Tmr.TimerEnable := SPRAY;
17 Pls_Tmr.PRE := 1000;
18 TONR(Pls_Tmr);
19
```

Example_12_2

Label does not exist	1
MainTask	
MainProgram	
MainRoutine	
Ladder Diagram	2
Mark_Def_Parts	
Structured Text	3